

Abstract of the Invention

A process used during manufacture of printed circuit boards comprises protecting metal pads and/or through-holes to provide a tarnish-resistant and solderable coating. In the method, the pads and/or through-holes are bright-etched, metal plated, preferably by an immersion process, and treated with a tarnish inhibitor. The tarnish inhibitor may be incorporated into the immersion plating bath. The metal plating is usually with silver or bismuth and the pads and/or through-holes comprise copper.

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